

FIG. 1

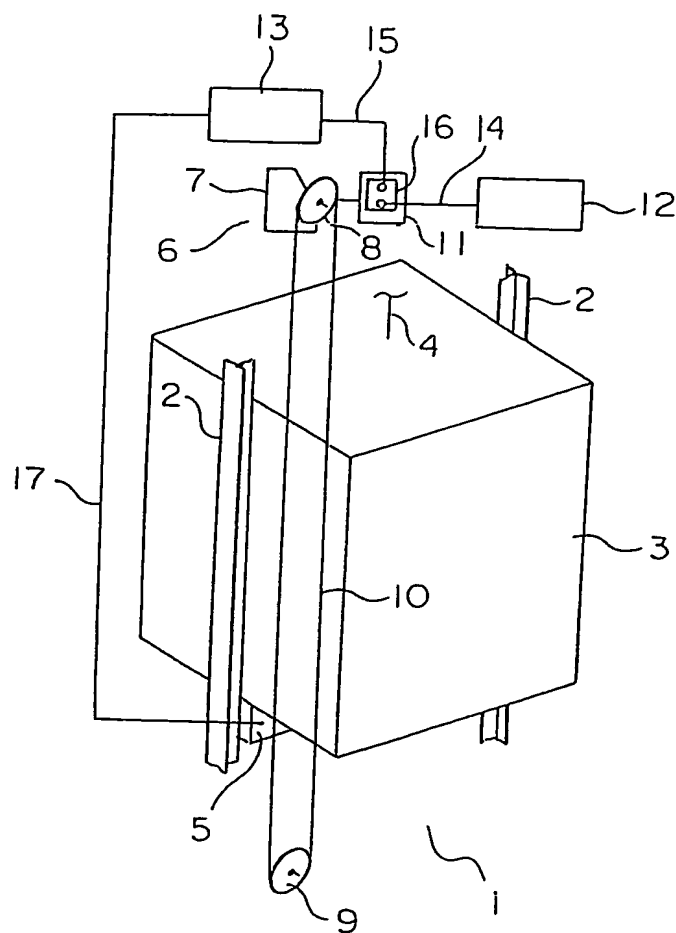


FIG. 2

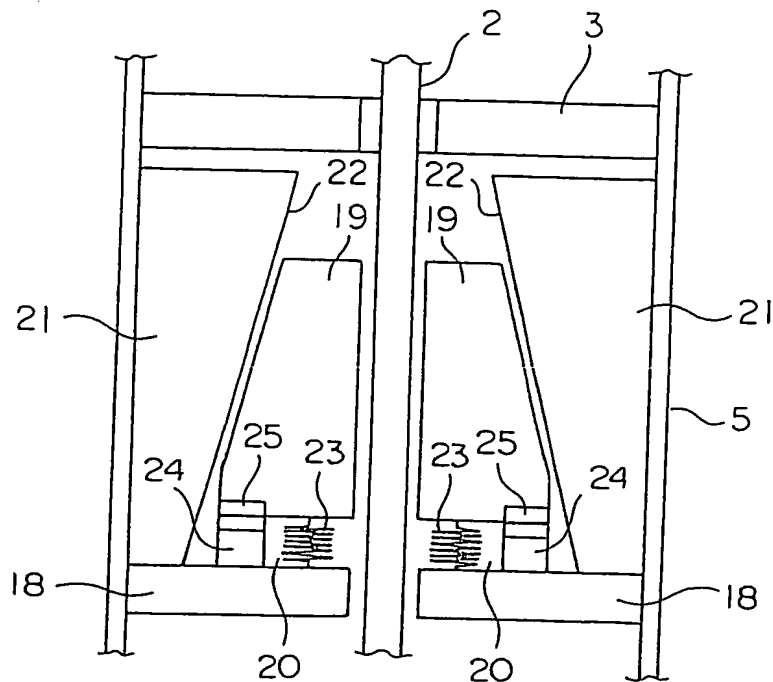


FIG. 3

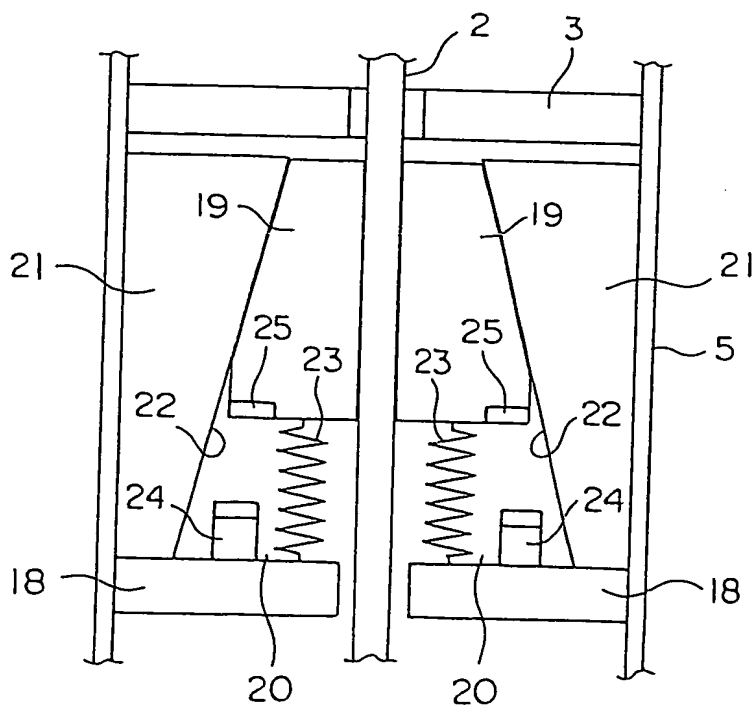


FIG. 4

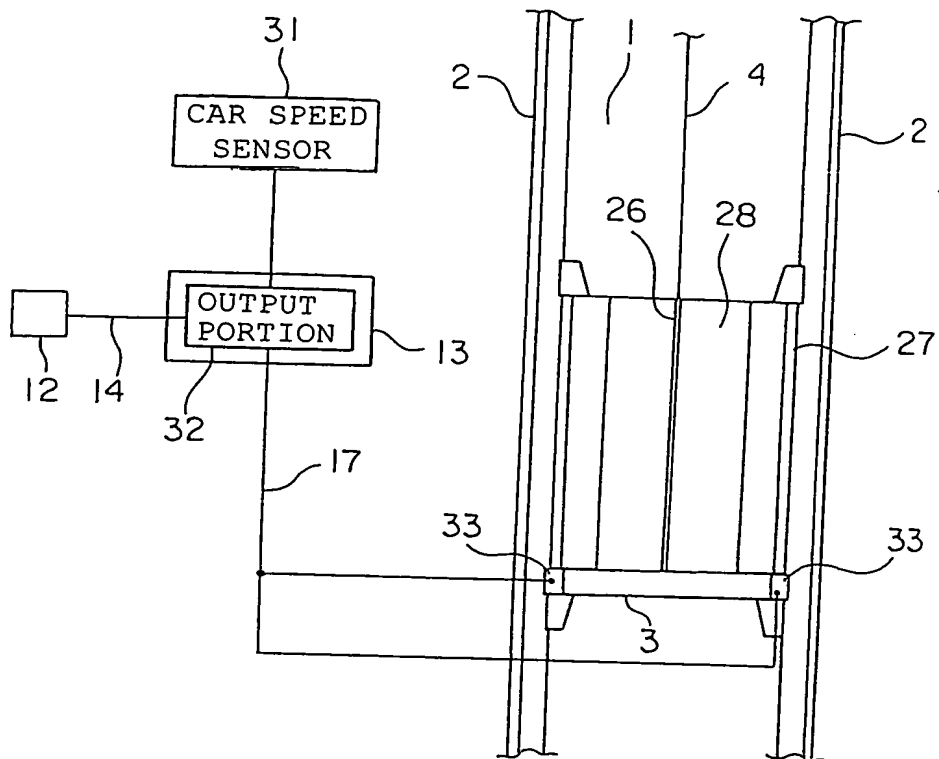


FIG. 5

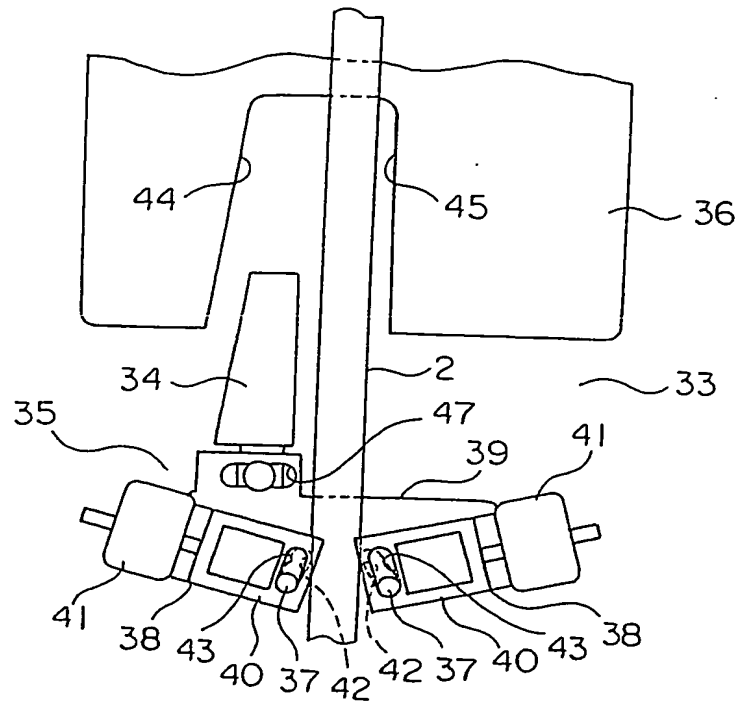


FIG. 6

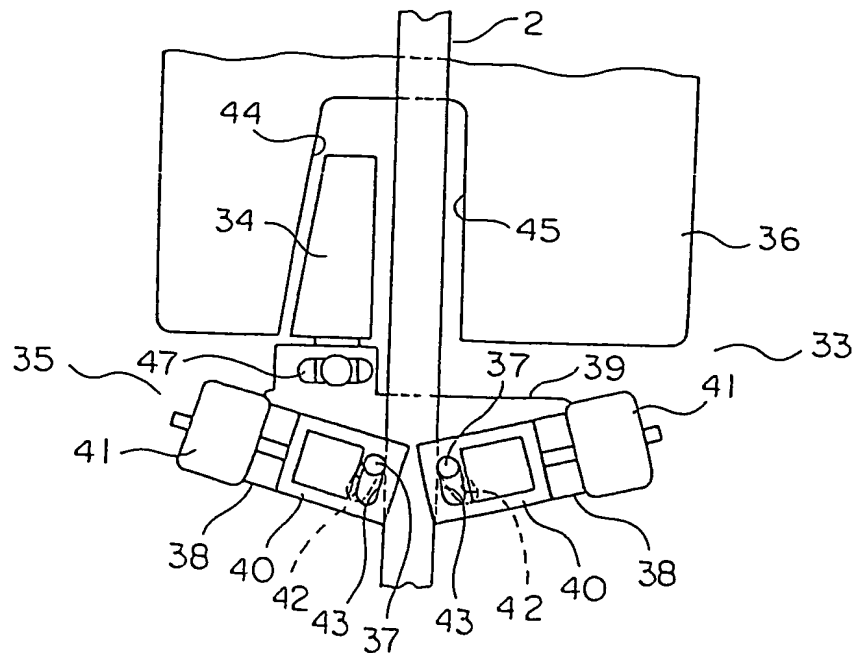


FIG. 7

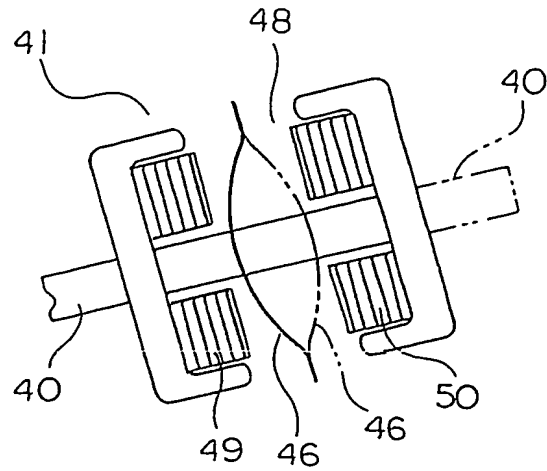


FIG. 8

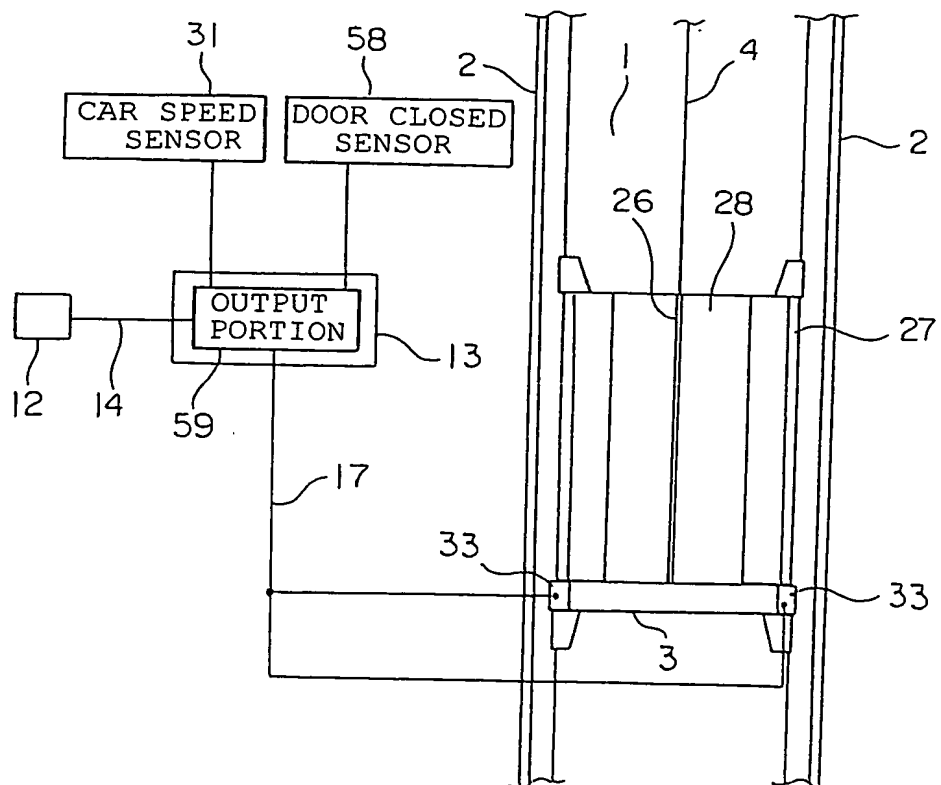


FIG. 9

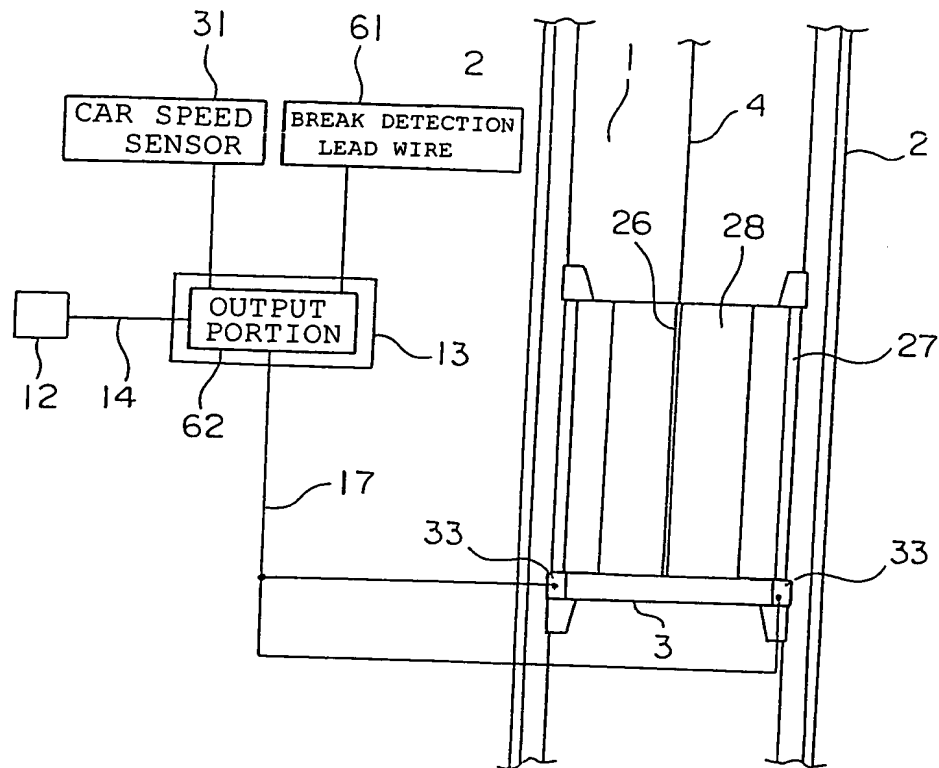


FIG. 10

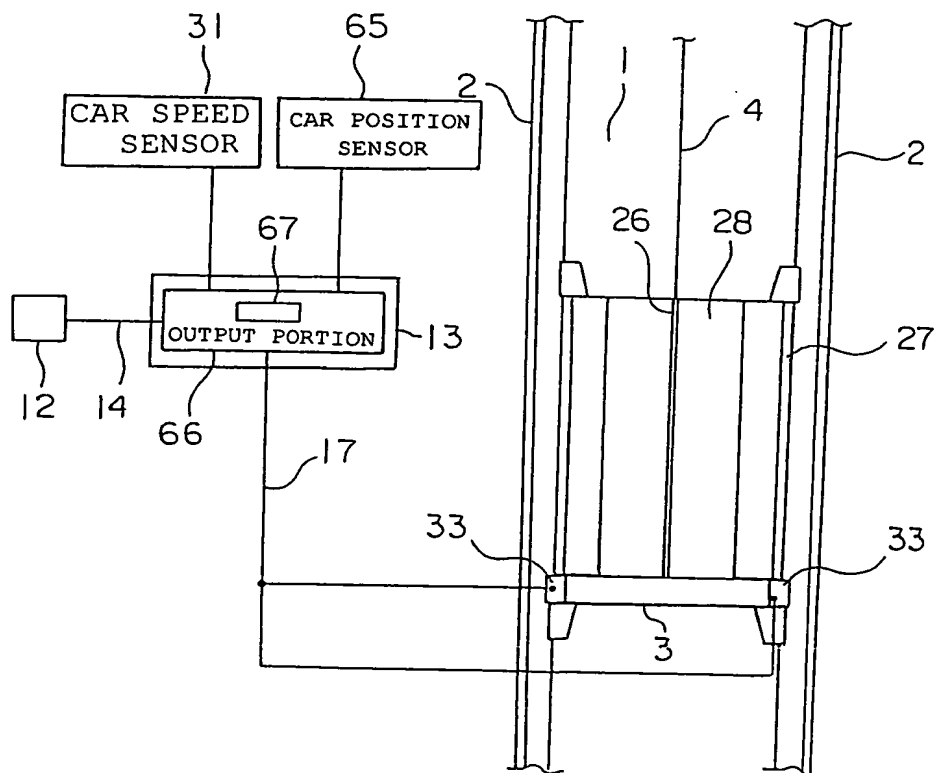


FIG. 11

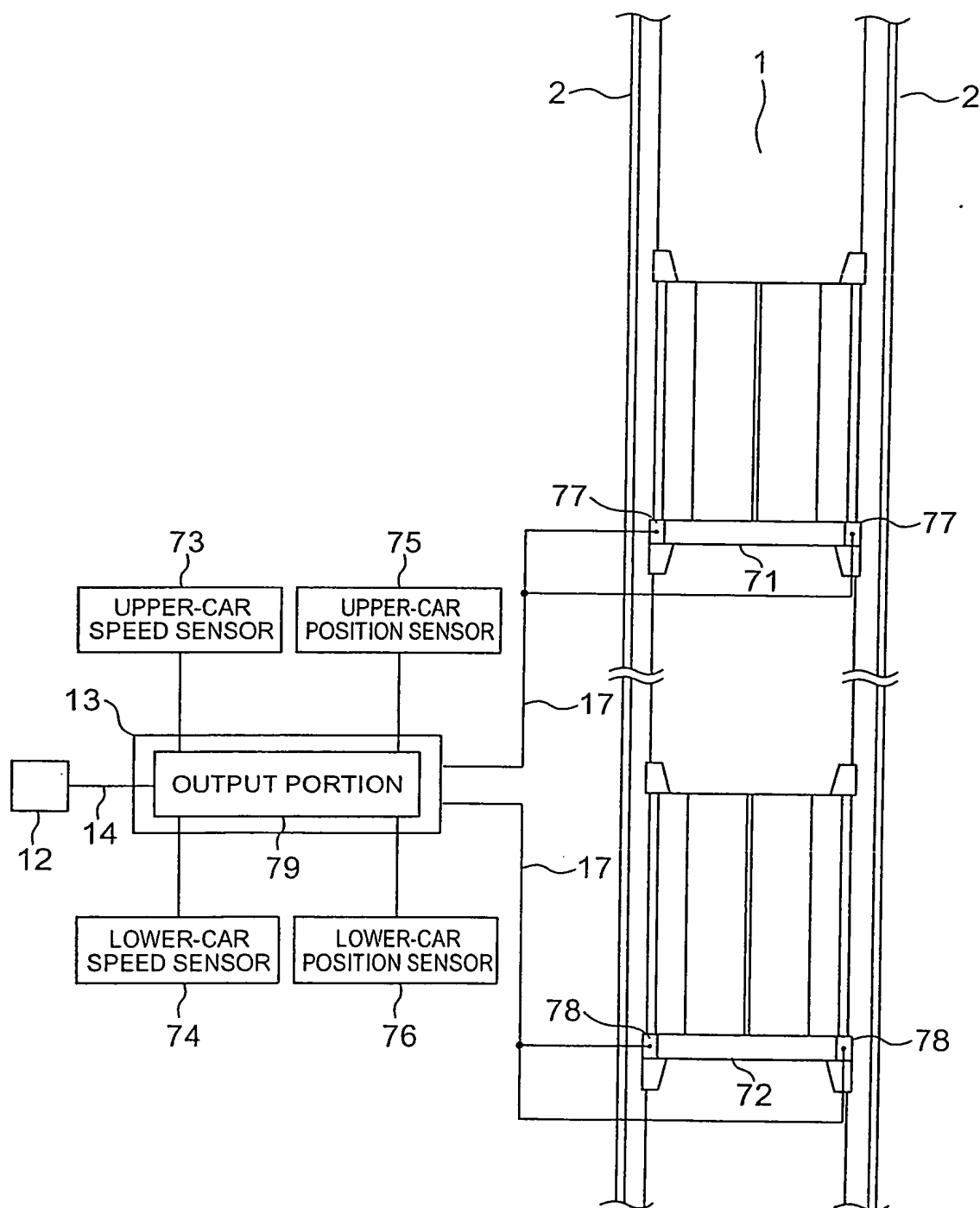


FIG. 12

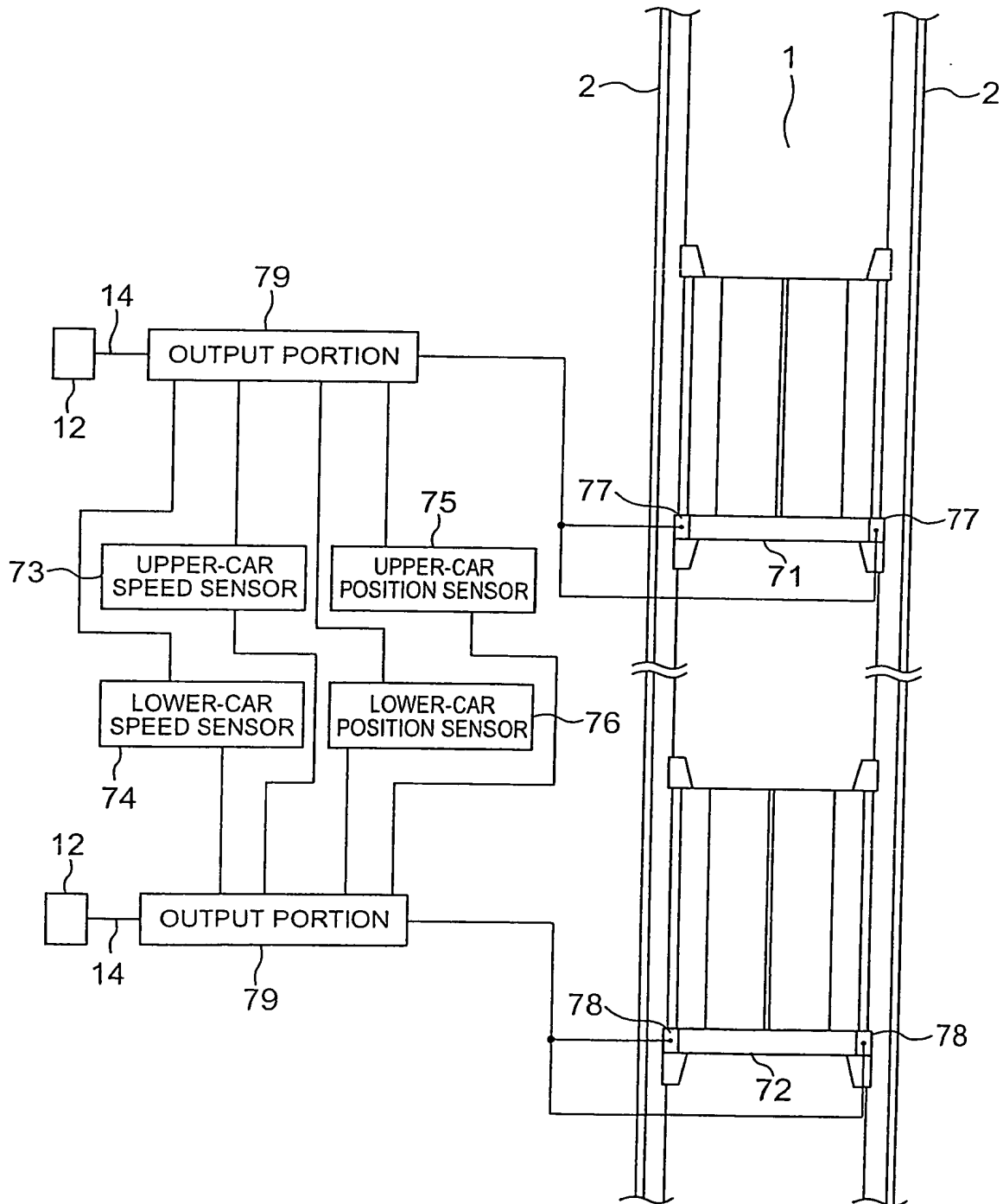


FIG. 13

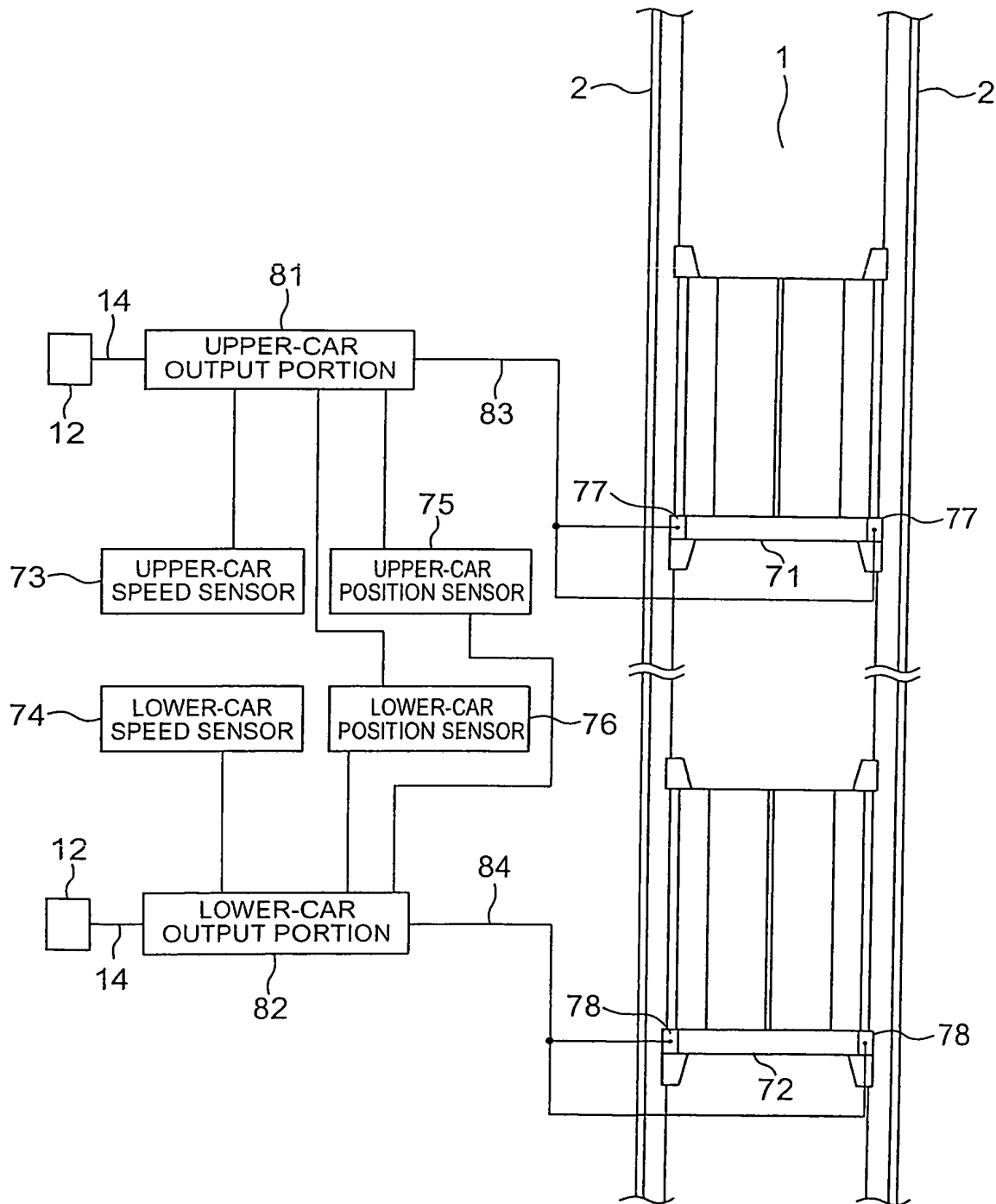


FIG. 14

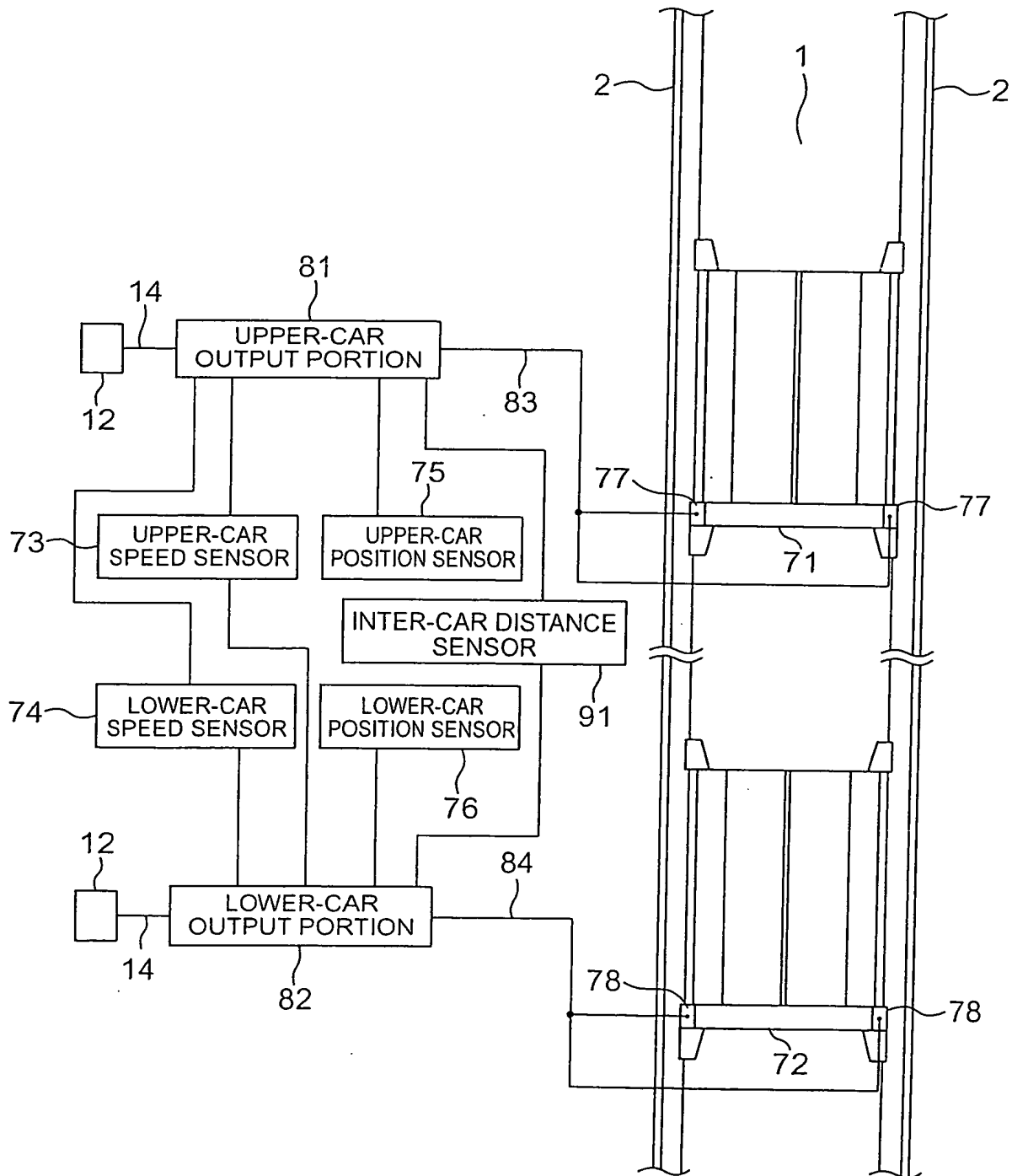


FIG. 15

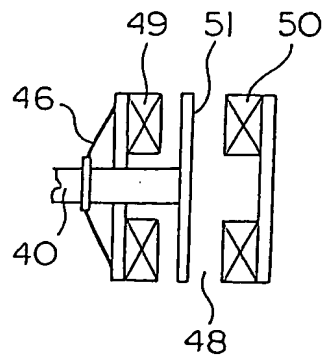


FIG. 16

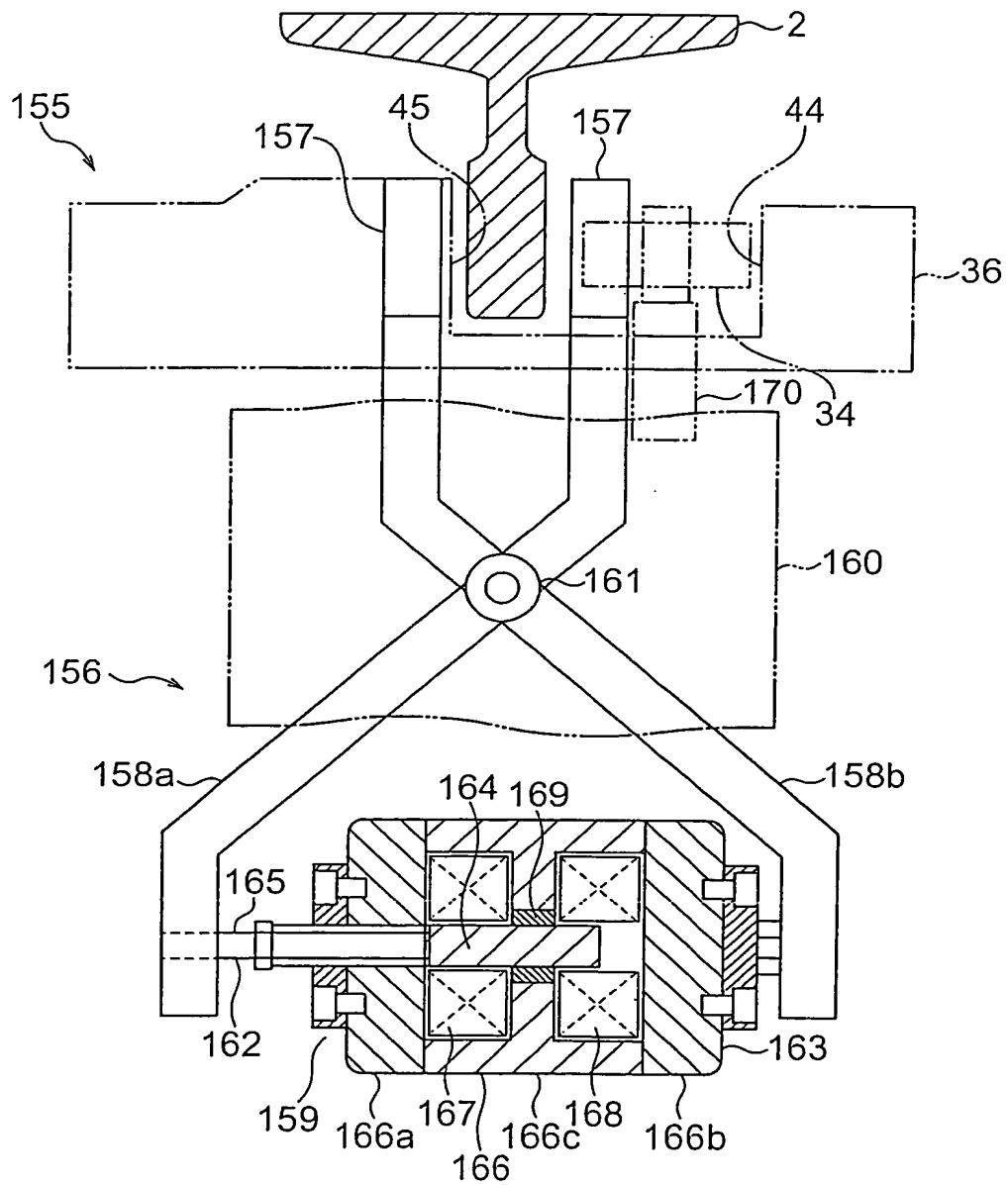


FIG. 17

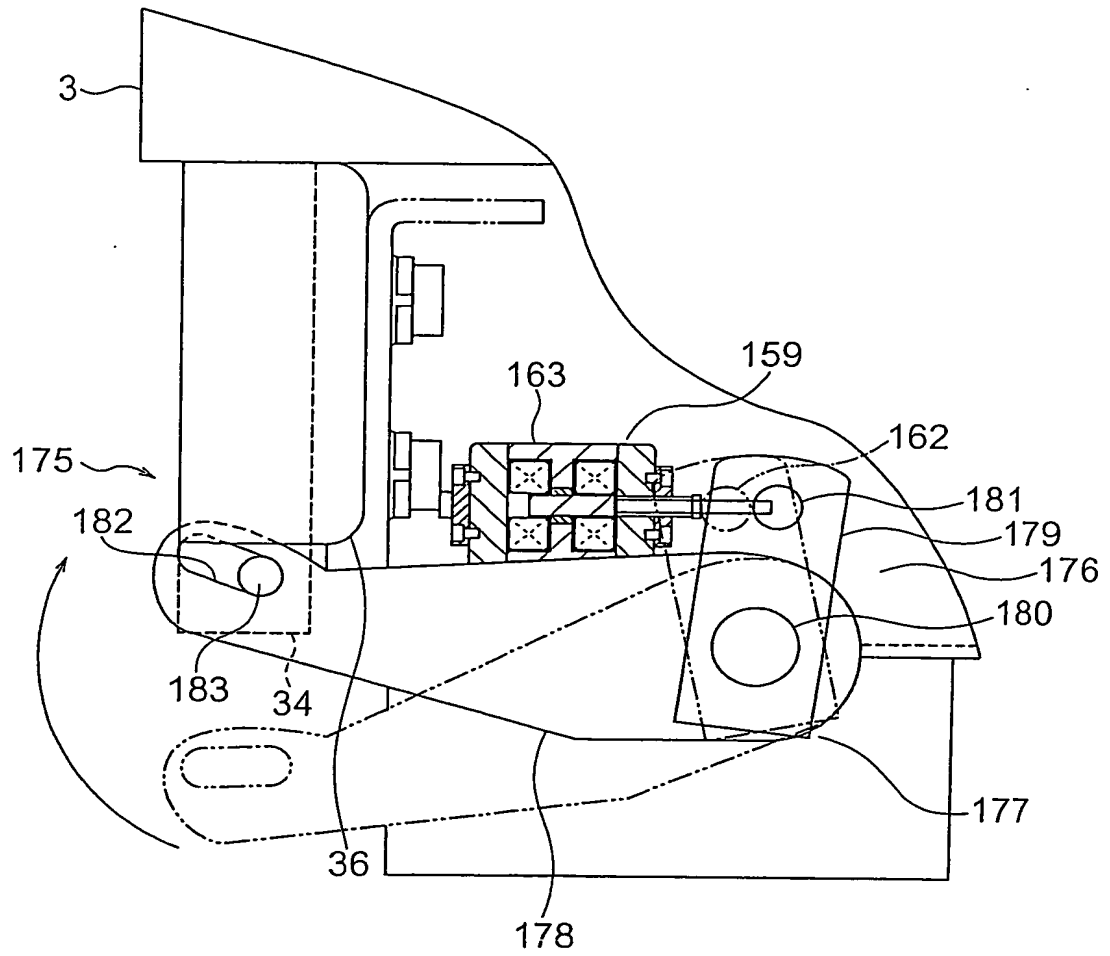


FIG. 18

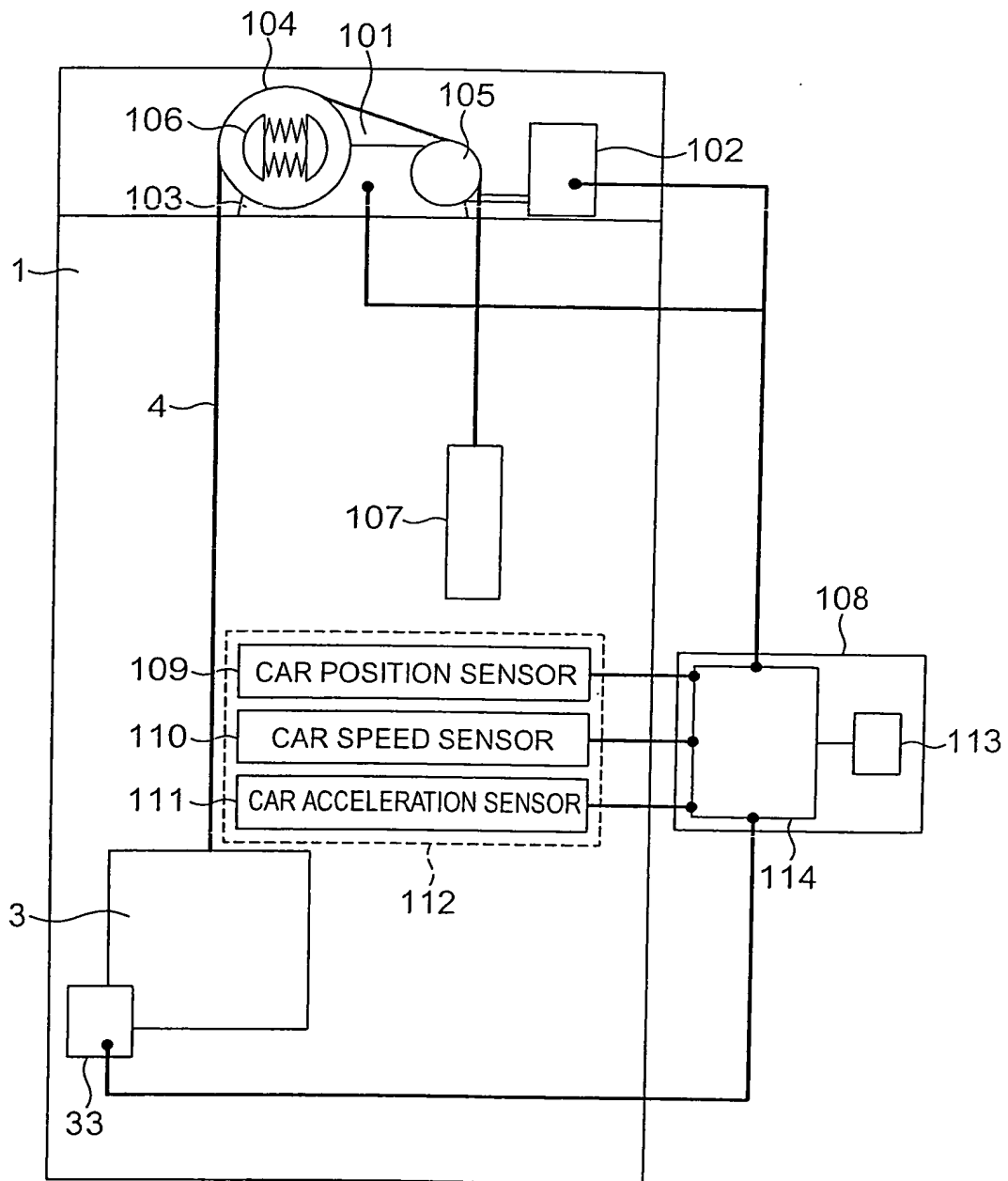


FIG. 19

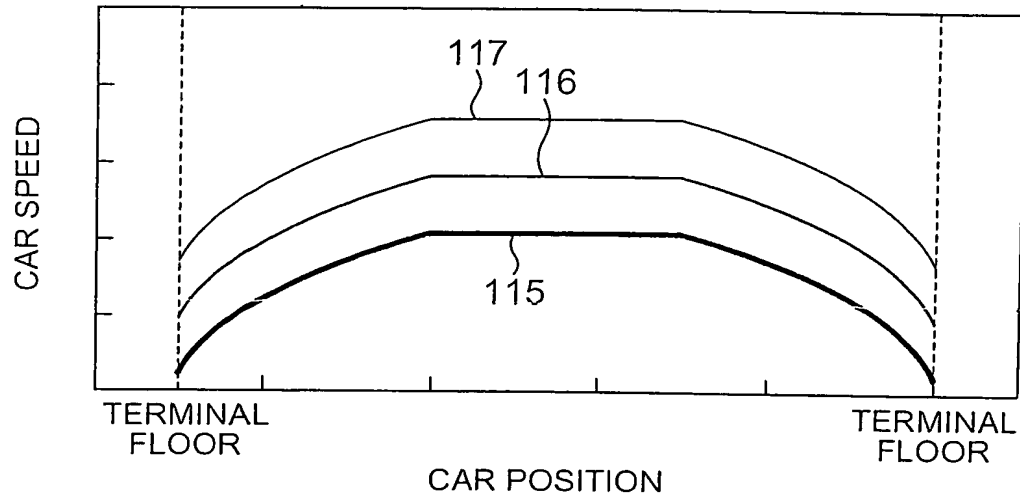


FIG. 20

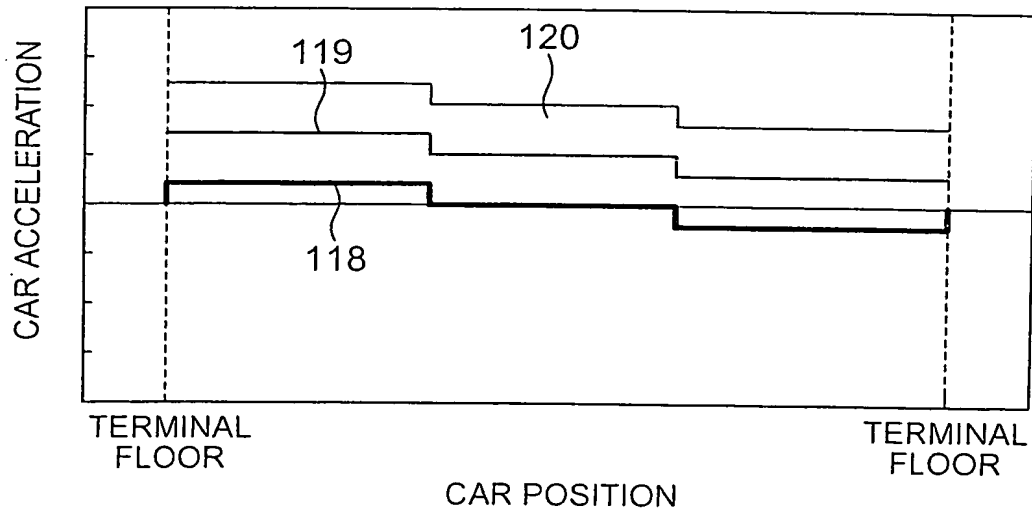


FIG. 21

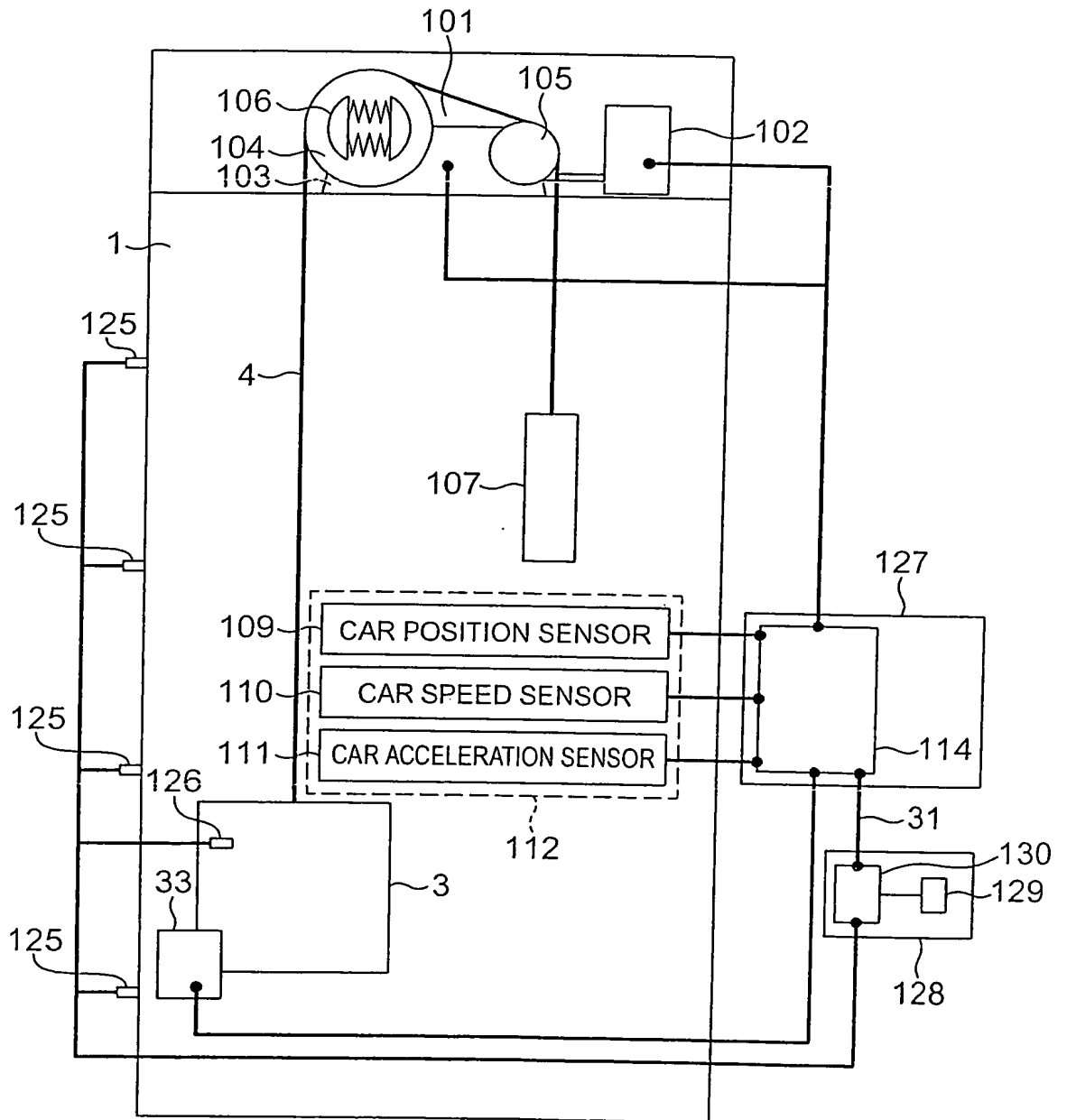


FIG. 22

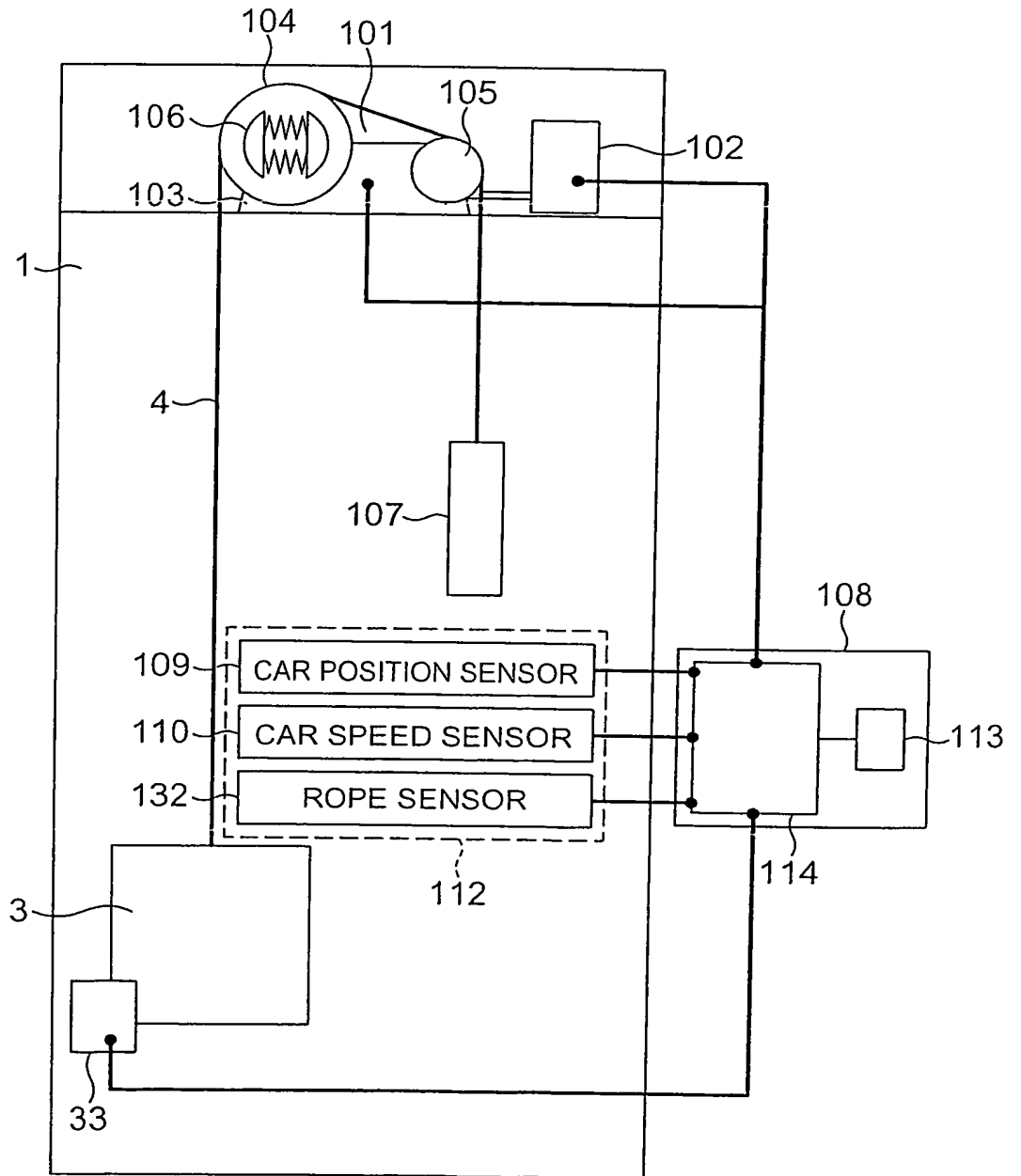


FIG. 23

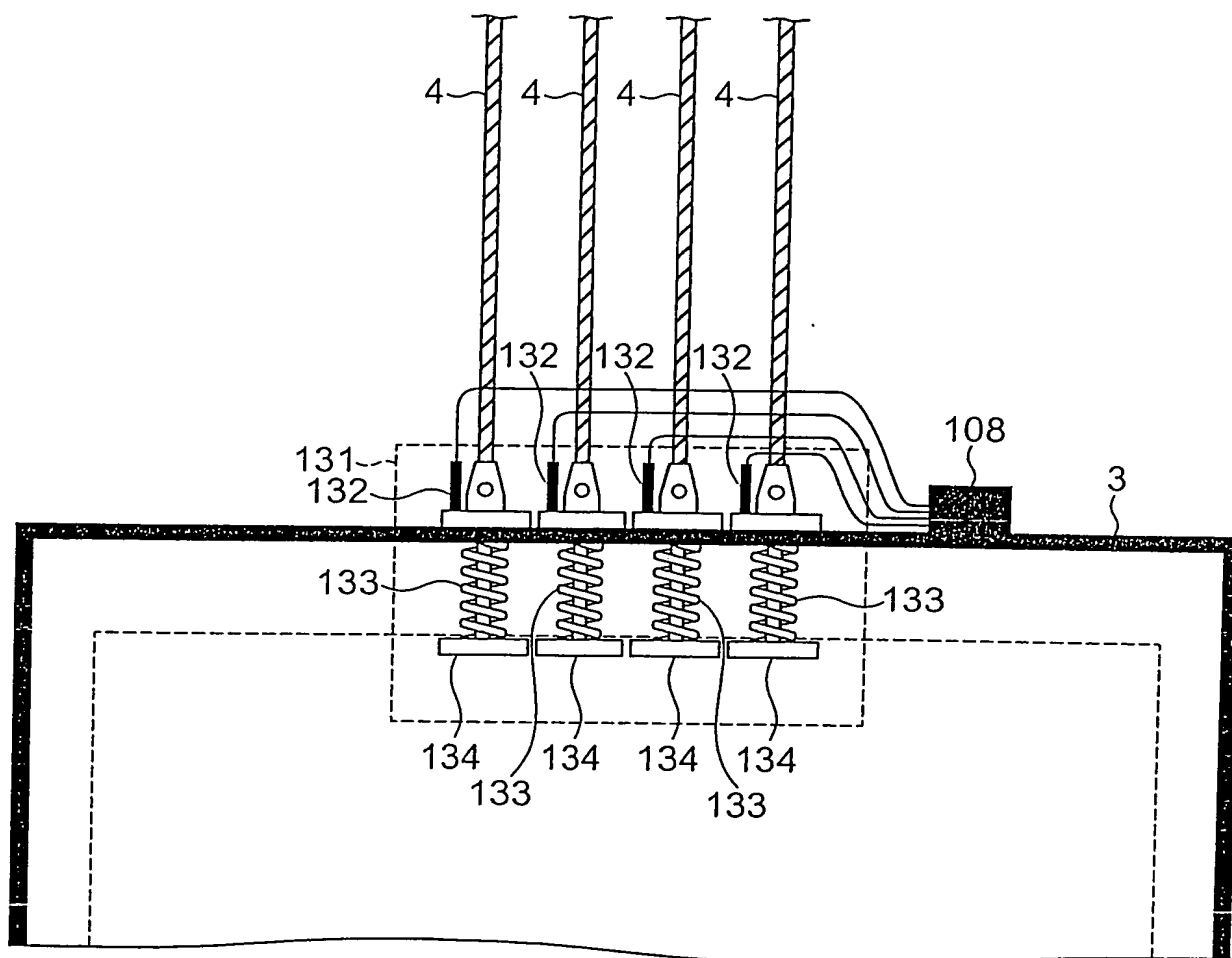


FIG. 24

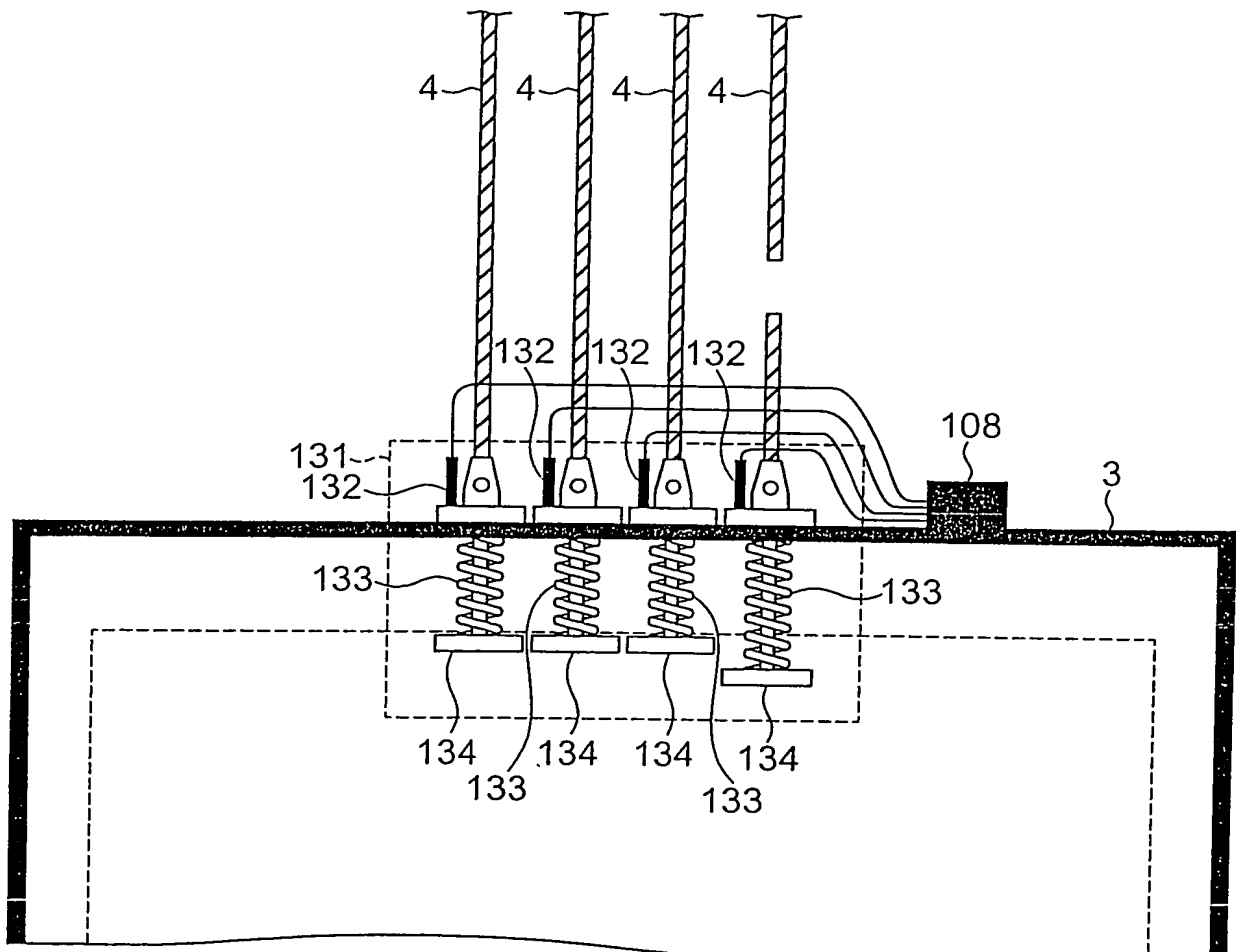


FIG. 25

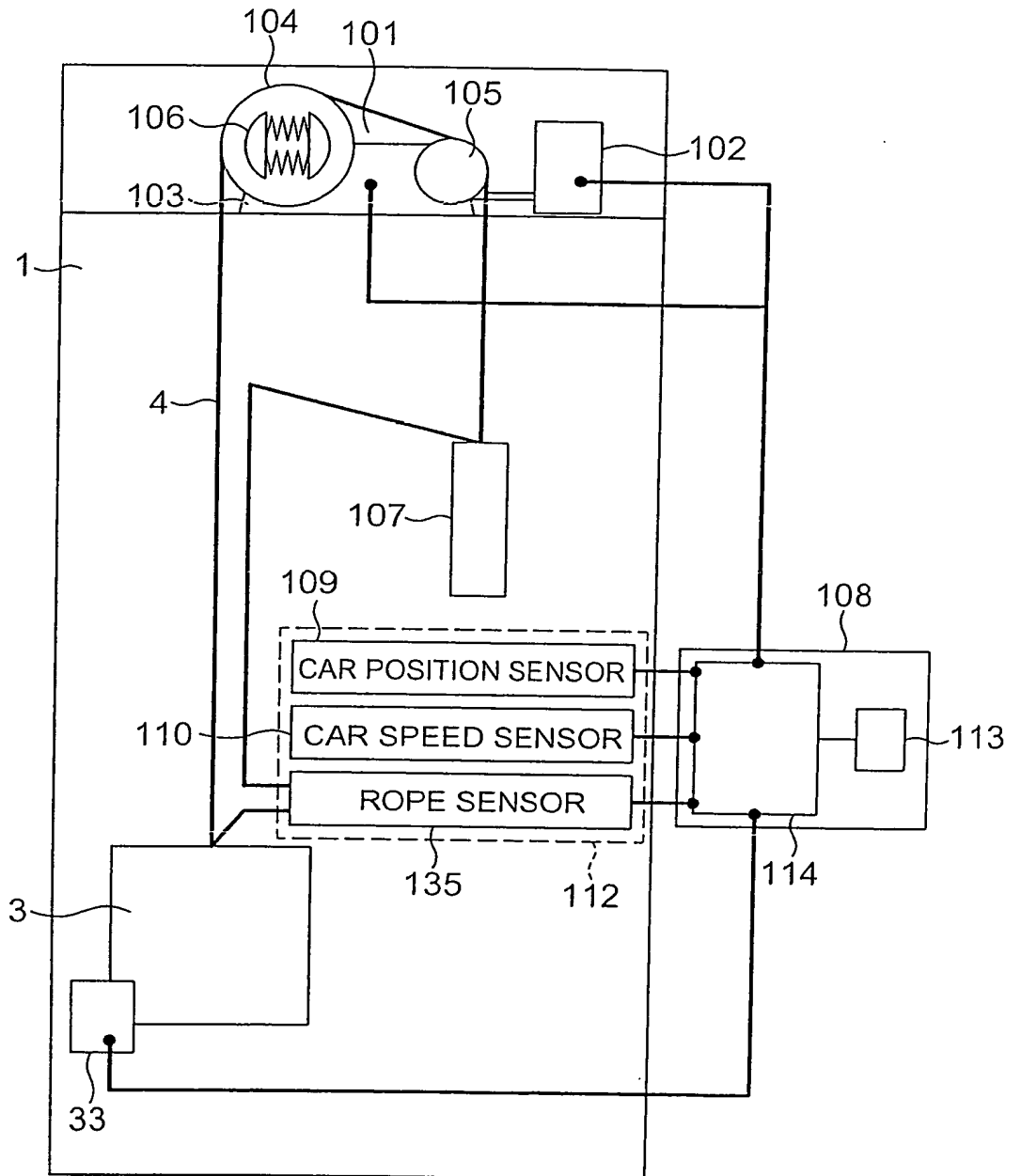


FIG. 26

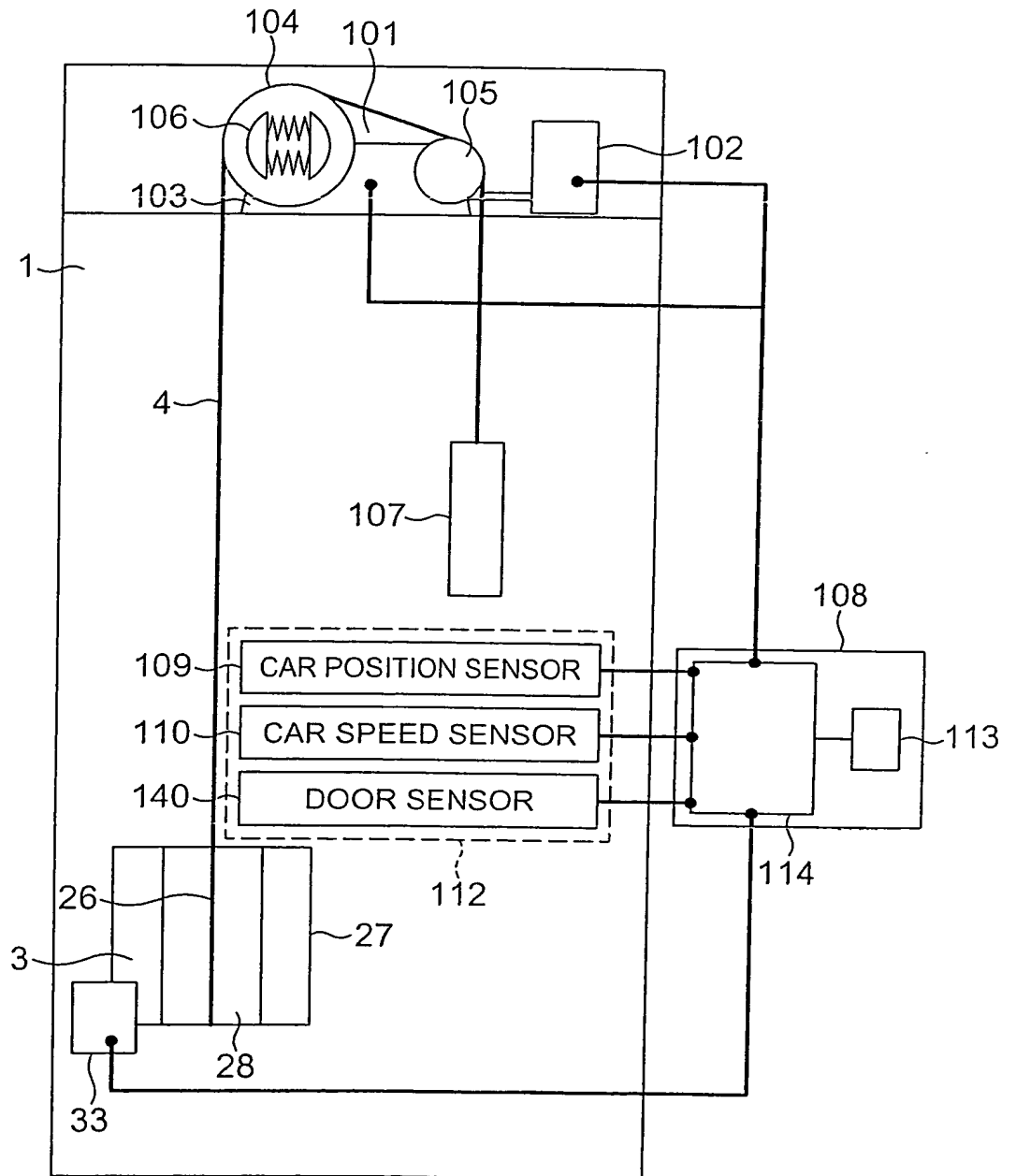


FIG. 27

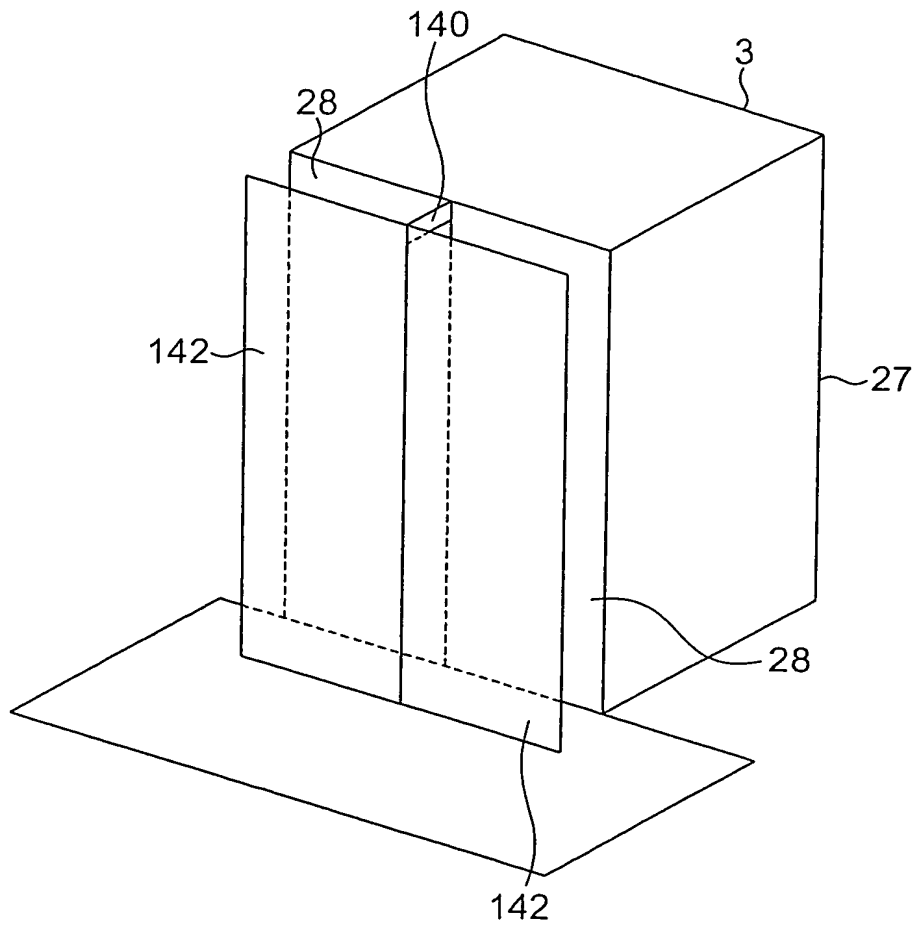


FIG. 28

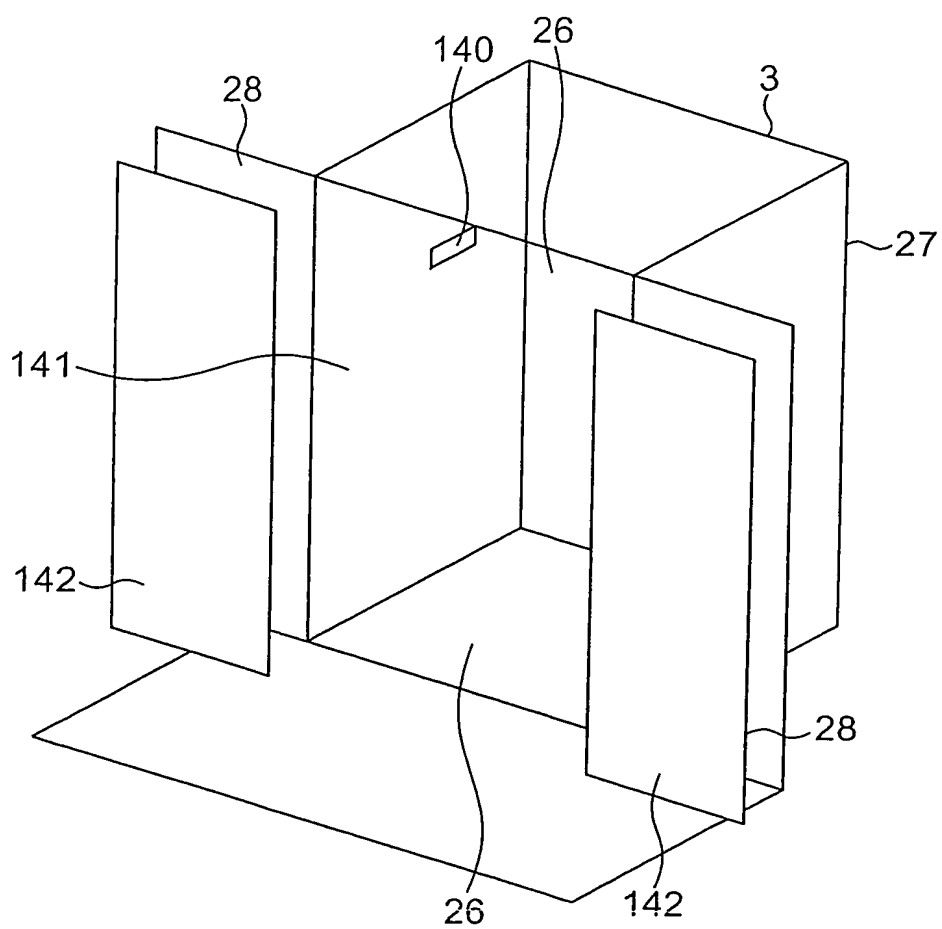


FIG. 29

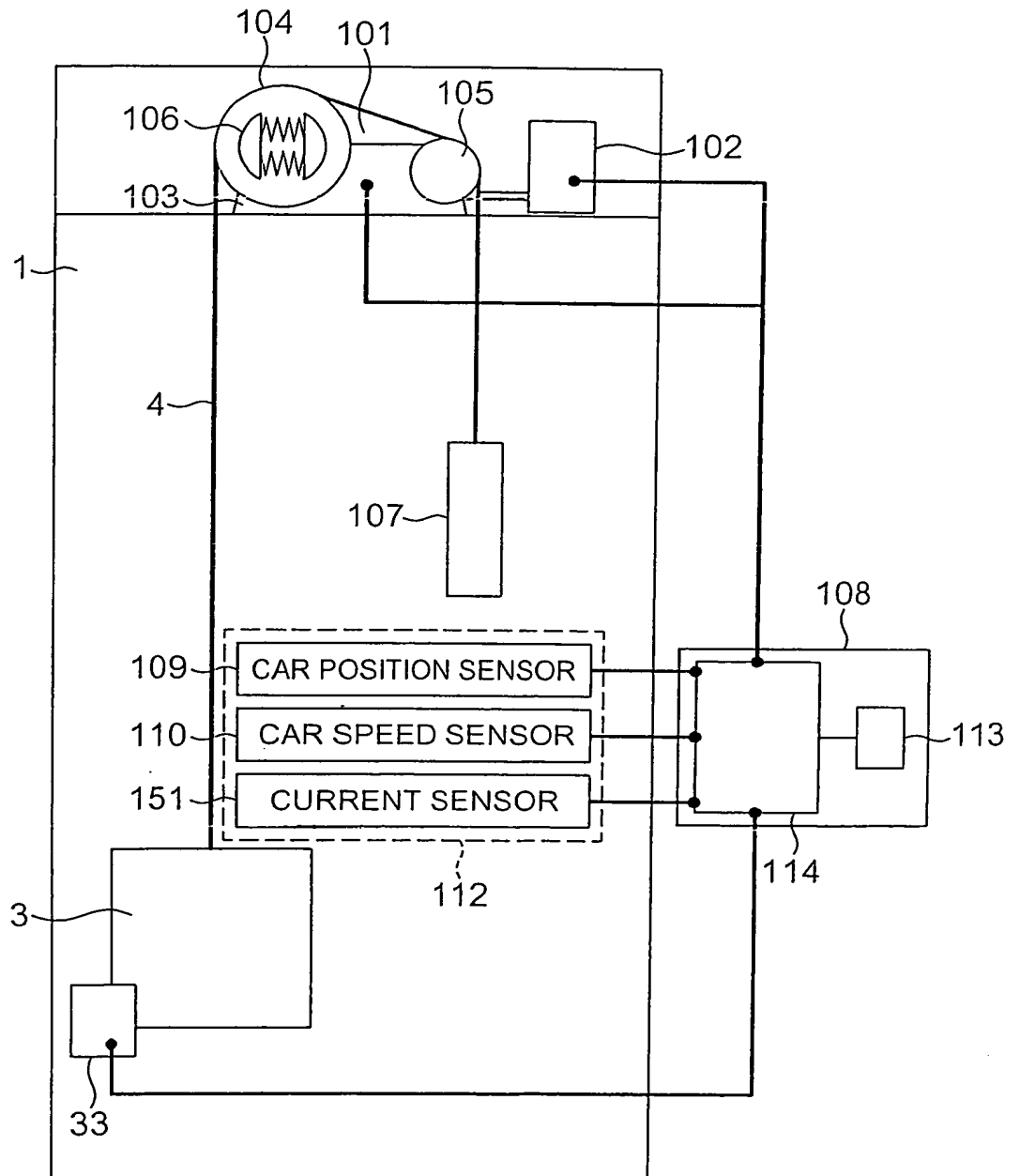


FIG. 30

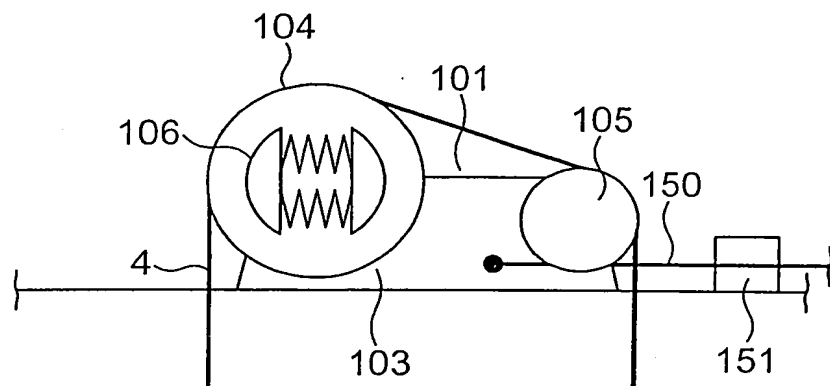


FIG. 31

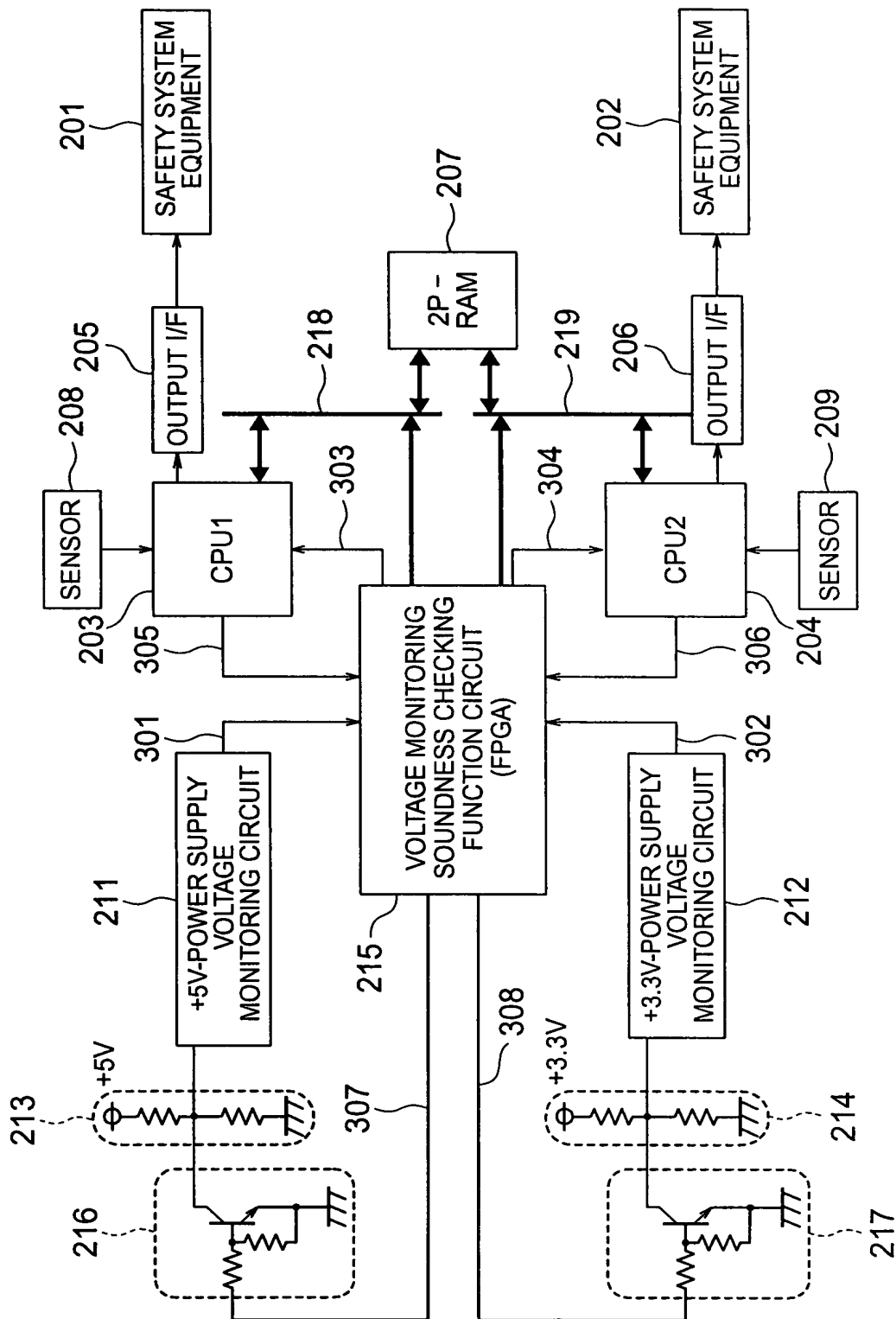


FIG. 32

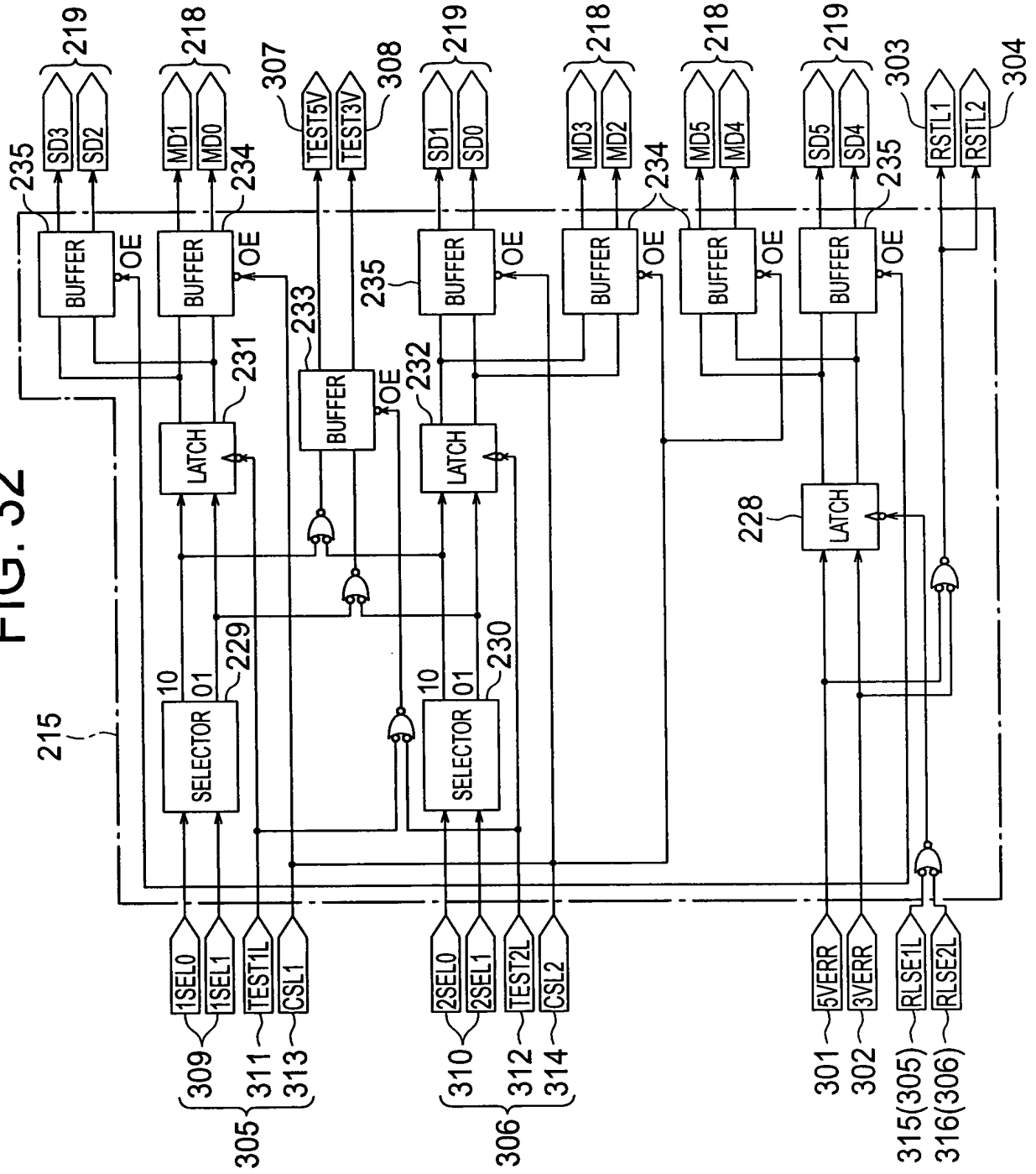


FIG. 33

DATA BIT	MD5 (SD5)	MD4 (SD4)	MD3 (SD3)		MD2 (SD2)	MD1 (SD1)	MD0 (SD0)
	<ABNORMAL VOLTAGE CRITERIAL VALUE>		<SELECTED VOLTAGE VALUE FOR CHECKING SOUNDNESS (OF CPU ON THE OTHER SIDE)>		<SELECTED VOLTAGE VALUE FOR CHECKING SOUNDNESS (OF CPU ON OWN SIDE)>		
MEANING OF VALUE	MD5 (SD5) : 0→3.3V ABNORMAL VOLTAGE 1→NORMAL VOLTAGE		MD3 (SD3)		MD2 (SD2)		SELECTED VOLTAGE
	MD4 (SD4) : 0→5V ABNORMAL VOLTAGE 1→NORMAL VOLTAGE		0	1	3.3V	1	3.3V
			1	0	5V	0	5V
			OTHER THAN ABOVE-MENTIONED COMBINATION		NOT SELECTED	OTHER THAN ABOVE-MENTIONED COMBINATION	
						NOT SELECTED	

FIG. 34

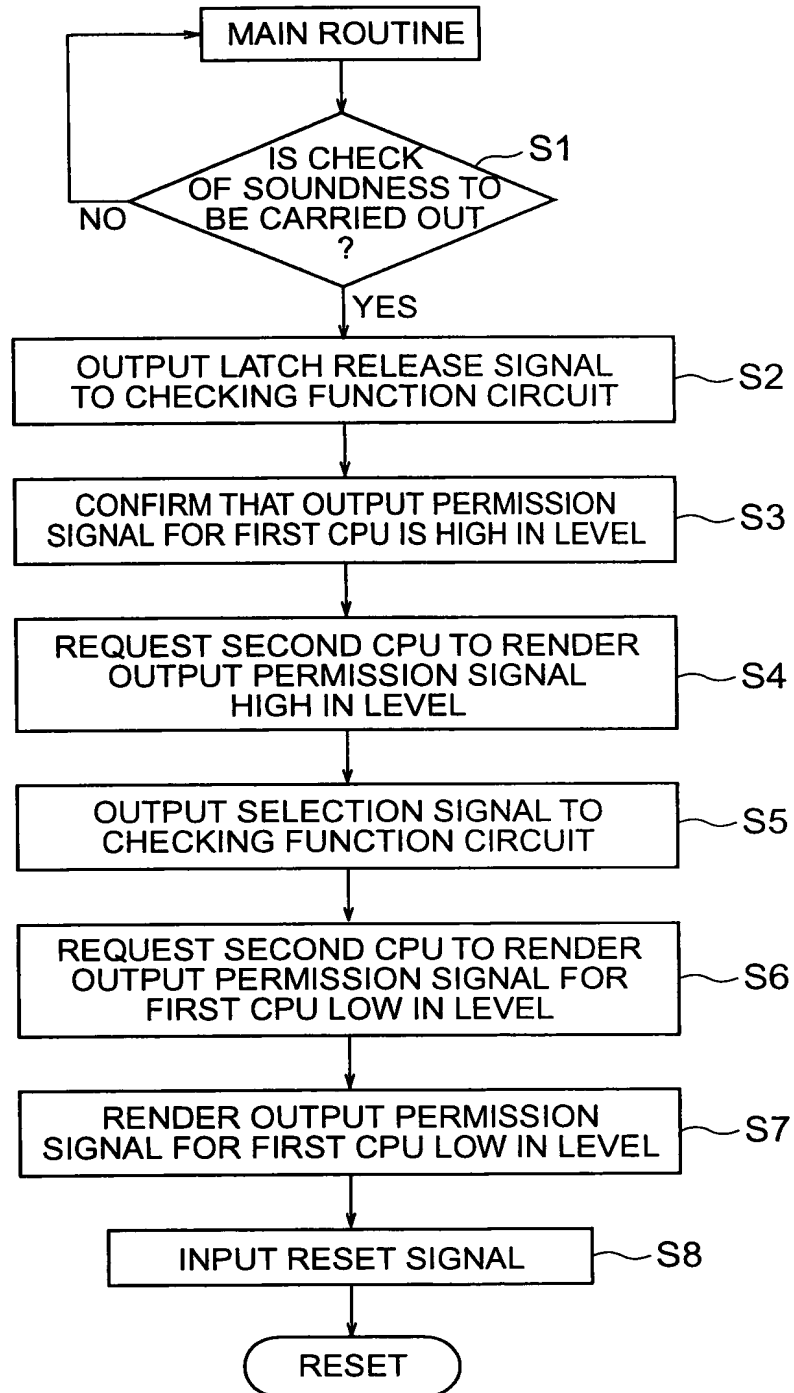


FIG. 35

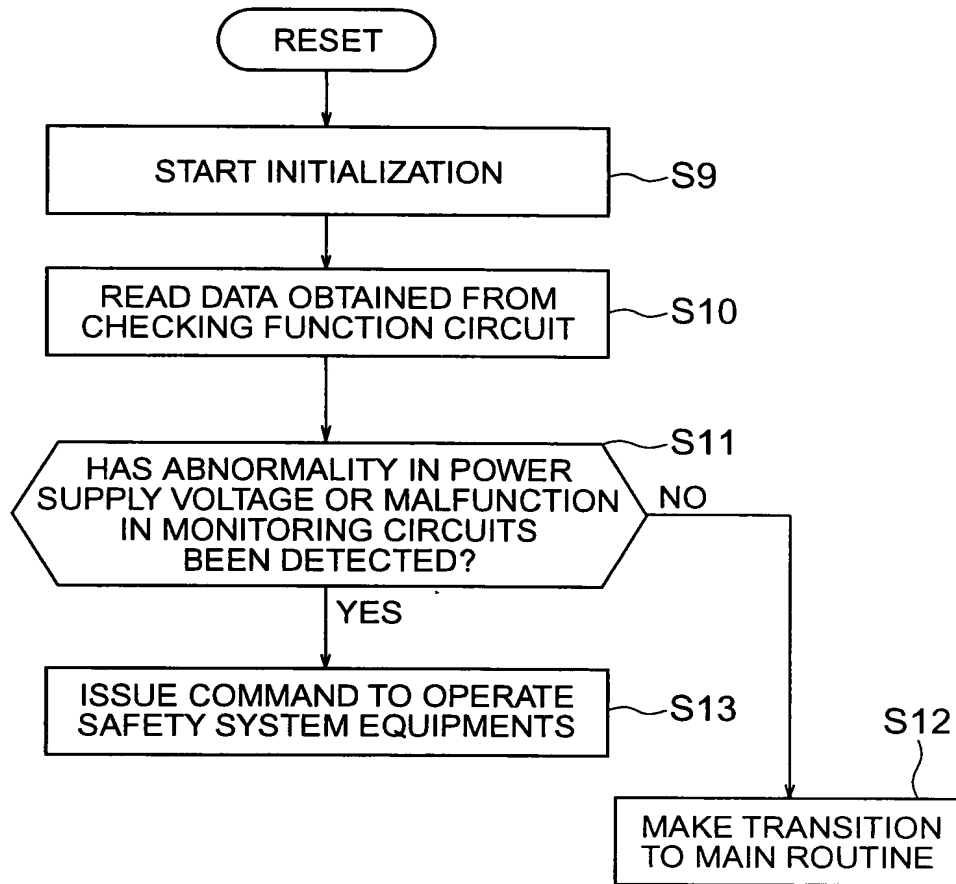


FIG. 36

